

IN THE UNITED STATES PATENT OFFICE

Applicant: Tao Pi et al.
Assignee: Xilinx, Inc.
Title: "FPGA Lookup Table with Transmission Gate
Structure for Reliable Low-Voltage Operation"
Serial No.: Not Yet Known File Date: 10-24-03
Examiner: Not Yet Known Art Unit: Not Yet Known
Divisional of
Serial No.: 10/241,094 File Date: 09-10-02
Docket No.: X-1128-1D US

Mail Stop PATENT APPLICATION
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the two (2) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

All of these references were cited in prior related U.S. patent application Serial Number 10/241,094 filed September 10, 2002 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,



Lois D. Cartier
Agent for Applicants
Reg. No. 40,941

Substitute for form 1449A/PTO

(use as many sheets as necessary)

Complete if Known

Application / Conf. No.	Unknown /
Filing Date	October 24, 2003
First Named Inventor	Tao Pi
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	X-1128-1D US

[illegible][illegible]

Date Considered

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, P.O. Box 1450, Alexandria, Virginia, 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia, 22313-1450.**

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application / Conf. No.	Unknown /
				Filing Date	October 24, 2003
				First Named Inventor	Tao Pi
				Art Unit	Unknown
				Examiner Name	Unknown
				Attorney Docket Number	X-1128-1D US
Sheet	2	of	2		

[illegible]

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.